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MI

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** CONTINUING DATA *****

This application is a CON of 10/387,478 03/14/2003 PAT 6,728,826
 which is a CON of 09/879,960 06/14/2001 PAT 6,549,974
 which is a CON of 09/706,843 11/07/2000 PAT 6,457,092
 which is a CON of 09/006,486 01/13/1998 PAT 6,145,050
 which is a CON of 08/669,914 06/25/1996 PAT 5,809,515
 which is a CON of 08/079,550 06/22/1993 PAT 5,530,828

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** FOREIGN APPLICATIONS *****

JAPAN 4-163074 06/22/1992

MI

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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MI

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	JAPAN	8	15	1
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ADDRESS

020457
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TITLE

Semiconductor storage apparatus in which different erase commands are sequentially sent to a plurality of nonvolatile semiconductor memories